

JOINT ELECTRON DEVICE ENGINEERING COUNCILS

JEDEC Electron Tube Council

JEDEC Solid State Products Council



2001 Eye Street, NW
Washington, DC 20006

Announcement

of

Electron Device Type Registration

Release No. 1456B

October 2, 1973

The Joint Electron Device Engineering Council announces the registration of the following electron device designation

5R4GA

on the application of the

JT-16 Committee on Government
Specifications and Standards

Type 5R4GA is similar to type 5R4GYA as registered in Release No. 1456, dated May 2, 1955, except that a low loss base is not specified.

NOTE: The JT-16 Committee recommends that this type be used to replace the 5R4GYA, which is the same except for base material.

Sheldon 5R4-G

Full-Wave High-Vacuum Rectifier (Tentative Data)

The 5R4-G is a coated filament type of Full-Wave, High-Vacuum Rectifier having a maximum peak plate current rating of 650 Milliamperes and a maximum peak inverse voltage rating of 2800 volts.

ELECTRICAL

Filament Voltage (AC or DC) (1) .. 5.0 Volts
 Filament Current 2.0 Amps.

MECHANICAL

Bulb ST 16
 Base Medium Shell Octal, 5 Pin
 Jetec Designation B5-15
 Mounting Position Vertical (3)

MAXIMUM RATINGS AS FULL-WAVE RECTIFIER (Design Center Values)

Peak Inverse Voltage (No Load Conditions) 2800 Max. Volts
 Peak Plate Current 650 Max. MA.

WITH CONDENSER - INPUT FILTER:

A-C Plate Voltage Per Plate (RMS)
 Full Load 700 900 Volts
 No Load 750 1000 Volts

WITH CHOKE - INPUT FILTER:

A-C Plate Voltage Per Plate (RMS)
 Full Load 750 950 Volts
 No Load 850 1000 Volts

TOTAL EFFECTIVE PLATE-SUPPLY IMPEDANCE

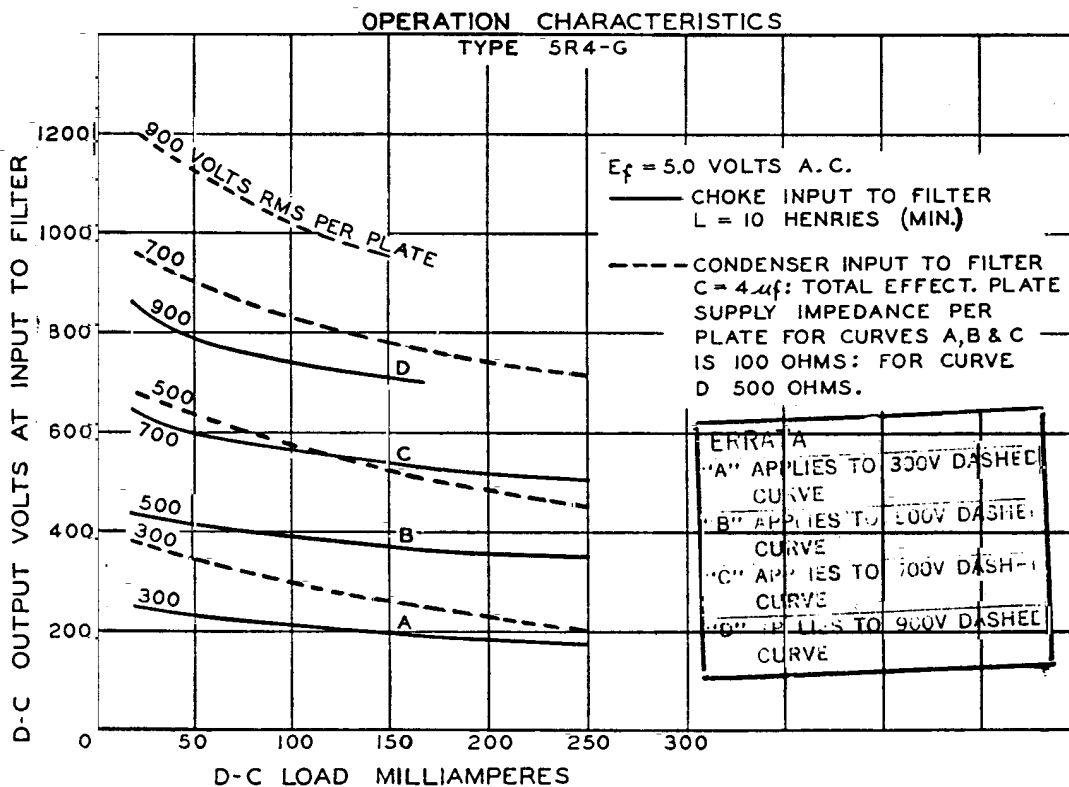
Per Plate (2) 125 575 Ohms
 D-C Output Current 250 Max. 150 Max. MA.

INPUT-CHOKE INDUCTANCE

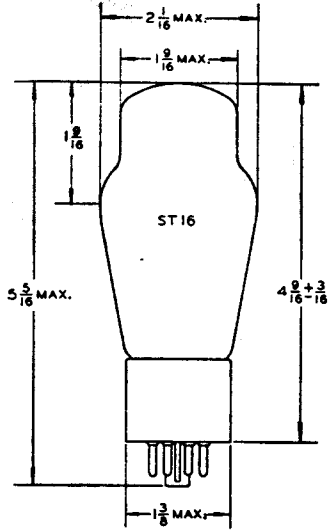
5 Min. 10 Min.
 Henries
 D-C Output Current 250 Max. 175 Max. MA.

NOTES

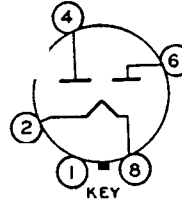
- (1) See curve of operating characteristics for restrictions on simultaneous application of filament and plate voltages.
- (2) When an Input-Filter capacitor larger than 4 mfd is used, it may be necessary to use more Plate-Supply Impedance to limit the peak plate current to the rated value.
- (3) Horizontal operation is permissible if pins 1 and 4 are in a vertical plane.



OUTLINE DRAWING



BASING DIAGRAM

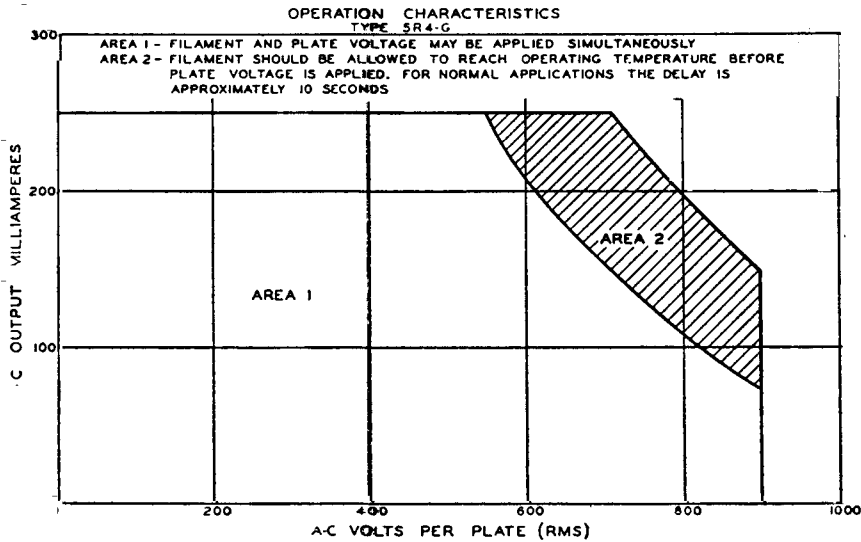
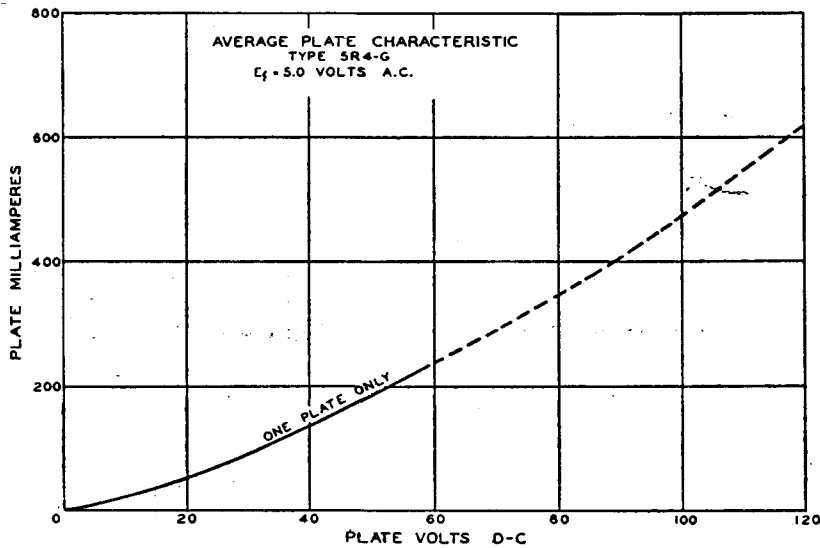


RTMA DESIGNATION

5T

CONNECTIONS

- Pin 1 - No Connection
- Pin 2 - Filament
- Pin 4 - Plate (Diode No. 2)
- Pin 6 - Plate (Diode No. 1)
- Pin 8 - Filament



SHELDON ELECTRIC CO.
 DIVISION OF ALLIED ELECTRIC PRODUCTS INC.
 68-98 COIT STREET, IRVINGTON 11, N. J.

